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Substitute for form 1449/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  Date Submitted: October 22, 2009  (use as many sheets as necessary)		Application Number	09/896,797
		Filing Date	6/29/2001
		First Named Inventor	John Trezza
		Art Unit	2613
		Examiner Name	Agustin Bello
Sheet 1	of 1	Attorney Docket Number	088245-4667

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US 6,235,590	08-04-1992	Basavanthally et al.	5, 135, 590
		US 5,394,498	02-28-1995	Hinterlong et al.	
		US 6,062,740	05-16-2000	Ohtsuka et al.	

CE 8/27/10

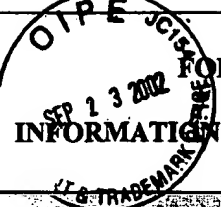
FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		JP 09 015088	01-17-1997	Sumitomo Electric Industries <i>English Abstract Provided</i>	
		WO 01/94995	12-13-2001	Calient Networks, Inc.	
		KR 100 340 665	05-31-2002	Mercury Corp.	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
		Copy of European Search Report for EP Patent Application No. 03748945.7 dated September 18, 2009.	
		Copy of Office Action for Korean Patent Application No. 7011845/2009 transmitted September 21, 2009.	

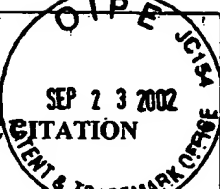
Examiner Signature	/Agustin Bello/ (06/30/2010)	Date Considered	
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 <b>FORM PTO-1449</b> <b>INFORMATION DISCLOSURE CITATION</b>			<b>Attorney Docket:</b> 4024-4008		<b>Serial No.:</b> 09/896,797	
			<b>Applicant:</b> John Trezza		<b>Group Art Unit:</b> 2872	
			<b>Filing Date:</b> June 29, 2001			
<b>U.S. PATENT DOCUMENTS</b>						
Examiner Initial	Patent Number	Publication Date	Name	Class	Sub-Class	Filing Date
	4,533,833		Copeland et al.			August 6, 1985
	5,266,794		Olbright et al.			November 30, 1993
	5,269,453		Melton et al.			December 14, 1993
	5,385,632		Goossen	<b>RECEIVED</b> <b>SEP 30 2002</b>		January 31, 1995
	5,477,933		Nguyen			December 26, 1995
	5,793,789		Ben-Michael et al.	Technology Center 2600		April 11, 1998
	5,814,889		Gaul			September 29, 1998
	6,005,262		Cunningham			December 21, 1999
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	Pub. No. US2001 0038103		Nitta et al			November 8, 2001

<b>Examiner</b>	<b>Date Considered</b>
<b>EXAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.	

<b>FORM PTO-1449</b>  <b>INFORMATION DISCLOSURE CITATION</b>			Attorney Docket:	Serial No.:
			4024-4008	09/896,797
			Applicant:	
			John Trezza	
Filing Date:		Group Art Unit:		
June 29, 2001		2872		

Pub. No. US2001-0081773	Inoue et al.	<b>RECEIVED</b> SEP 30 2002	June 27, 2002 <i>CE 8/27/02</i>
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### FOREIGN PATENT DOCUMENTS Technology Center 2600

Examiner Initial	Patent Number	Publication Date	Country	Class	Sub-Class	Translation
						<input type="checkbox"/> Yes <input type="checkbox"/> No
						<input type="checkbox"/> Yes <input type="checkbox"/> No
						<input type="checkbox"/> Yes <input type="checkbox"/> No

### OTHER DOCUMENTS (continued)

	Ahadian, J.F., et al., "Practical OEIC's Based on the Monolithic Integration of GaAs-InGap LED's with Commercial GaAs VLSI Electronics", IEEE Journal of Quantum Electronics, Vol. 34, No. 7, pages 1117-1123, July 1998.
	Alduino, A.C. et al., "Quasi-Planar Monolithic Integration of High-Speed VCSEL and Resonant Enhanced Photodetector Arrays", IEEE Photonics Technology Letters, Vol. 11, No. 5, pages 512-514, May 1999.
	<del>Anderson, B., "Rapid Processing And Properties Evaluation Of Flip-Chip Underfills", Dexter Electronic Materials, 9 pages.</del>
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